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## UTILITY PATENT APPLICATION TRANSMITTAL

ADDRESS TO:

Assistant Commissioner for Patents

Box Patent Application Washington, D. C. 20231

Sîr:

Transmitted herewith for filing is the patent application of:

INVENTOR(S): Marvin J. RICH and Ashutosh MISRA

TITLE: VHDL TECHNOLOGY LIBRARY METHOD FOR EFFICIENT CUSTOMIZATION OF CHIP

GATE DELAYS

#### ASSIGNEE INFORMATION PURSUANT TO 37 C.F.R. 1.76

ASSIGNEE NAME:

INTERNATIONAL BUSINESS MACHINES CORPORATION

ASSIGNEE RESIDENCE:

ARMONK, NEW YORK

In connection with this application, the following are enclosed:

XX Fee Transmittal Form

46 Pages of Specification, Claims and Abstract

15 Claims

22 Sheets of Formal Drawings (FIGS. 1-22)

XX Declaration and Power of Attorney

XX Information Disclosure Statement and Form PTO-1449 w/ 11 references

XX Assignment recordation sheet with assignment to: INTERNATIONAL BUSINESS MACHINES

CORPORATION

XX Please direct all correspondence to Customer Number 23334



Respectfully submitted,

FLEIT, KAIN, GIBBONS, GUTMAN & BONGINI P.L. One Boca Commerce Center 551 Northwest 77th Street, Suite 111 Boca Raton, FL 33487 (561) 989-9811 (561) 989-9812 Fax

BY:

Jose Gutman/

Reg. No. 35, 17/1

# FEE TRANSMITTAL

Transmitted herewith for filing is the patent application of:

INVENTOR(S): Marvin J. RICH and Ashutosh MISRA

TITLE: VHDL TECHNOLOGY LIBRARY METHOD FOR EFFICIENT CUSTOMIZATION OF CHIP

GATE DELAYS

The fee has been calculated as shown below. (Small entity fees indicated in parentheses.)

For	Number Filed		Number Extra	Rate Large (Small)	Basic Fee \$740 (\$370)
Total Claims	15	20	<u> </u>	\$18 (\$9)	0
Independent Claims	7	3	4	\$84 (\$42)	336
Multiple Dependent Claims				\$280 (\$140)	0
Assignment Recording Fee				\$40	40
				TOTAL FEE:	\$ 1,116

- XX The Commissioner is hereby authorized to charge Deposit Account No. <u>09-0463</u> in the amount of \$1116. A duplicate copy of this sheet is enclosed.
- XX The Commissioner is hereby authorized to charge payments of (1) any additional filing fees required under 37 CFR 1.16, and/or (2) any patent application processing fees under 37 CFR 1.17 associated with this application or credit any overpayment to Deposit Account No. <u>09-0463</u>

Respectfully submitted,

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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Atty Docket: POU920010166US1

Marvin J. RICH et al.

APPLICATIONS BRANCH

Serial No. (not yet assigned)

Filed: HEREWITH

For: VHDL TECHNOLOGY LIBRARY METHOD FOR EFFICIENT CUSTOMIZATION OF CHIP

GATE DELAYS

### CERTIFICATE OF EXPRESS MAIL MAILING

"Express Mail" Mailing Label No. EL863783632US Date of Deposit: January 2, 2002

**Box Patent Application Assistant Commissioner for Patents** Washington, D.C. 20231

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I hereby certify that

**Application Transmittal** 

Fee Transmittal Form

Specification, Claims, Abstract

1 set of 22 sheets of formal drawings

Declaration and Power of Attorney

Information Disclosure Statement and Form PTO-1449 w/ 11 refs.

Assignment Recordation Sheet with Assignment

Return postcard

are being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and are addressed to:

Box Patent Application **Assistant Commissioner for Patents** Washington, D.C. 20231

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